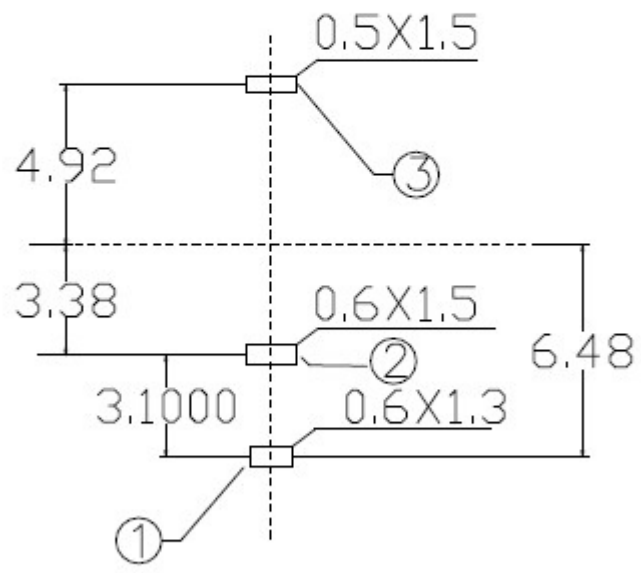
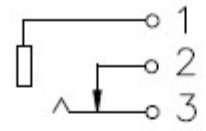


SCHEMATIC



PCB LAYOUT
(TOP VIEW)

